Refine Search

Search Results -

Terms	Documents
L1 same (cache near5 processor near5 memory near5 (peripheral or (I adj1 O) or (input adj1 output)))	15

Database:

```
US Pre-Grant Publication Full-Text Database
US Patents Full-Text Database
US OCR Full-Text Database
EPO Abstracts Database
JPO Abstracts Database
Derwent World Patents Index
IBM Technical Disclosure Bulletins
```

Search: L4

[Refine Search]

[Recall Text ⬍] [Clear] [Interrupt]

Search History

DATE: Tuesday, August 10, 2004   Printable Copy   Create Case

Set Name side by side	Query	Hit Count	Set Name result set
DB=USPT, USOC; PLUR=YES; OP=OR			
L4	L1 same (cache near5 processor near5 memory near5 (peripheral or (I adj1 O) or (input adj1 output)))	15	L4
L3	L1 and (cache near5 processor near5 memory near5 (peripheral or (I adj1 O) or (input adj1 output)))	93	L3
L2	L1 and (cache near5 processor)	878	L2
L1	((shared or common) adj1 memory) same (multiprocessor or (multi adj1 processor))	1810	L1

END OF SEARCH HISTORY

h   e b   b   cg b   e e ch

Refine Search

Search Results -

Terms	Documents
L4	0

Database:
```
US Pre-Grant Publication Full-Text Database
US Patents Full-Text Database
US OCR Full-Text Database
EPO Abstracts Database
JPO Abstracts Database
Derwent World Patents Index
IBM Technical Disclosure Bulletins
```

Search: L5

[Refine Search]

[Recall Text]   [Clear]   [Interrupt]

Search History

DATE: Tuesday, August 10, 2004   Printable Copy   Create Case

Set Name side by side	Query	Hit Count	Set Name result set
DB=EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR			
L5	L4	0	L5
DB=USPT,USOC; PLUR=YES; OP=OR			
L4	L1 same (cache near5 processor near5 memory near5 (peripheral or (I adj1 O) or (input adj1 output)))	15	L4
L3	L1 and (cache near5 processor near5 memory near5 (peripheral or (I adj1 O) or (input adj1 output)))	93	L3
L2	L1 and (cache near5 processor)	878	L2
L1	((shared or common) adj1 memory) same (multiprocessor or (multi adj1 processor))	1810	L1

END OF SEARCH HISTORY

h   e b   b cg b   e e ch